

Fig 1

Prior Art

00765885 014001

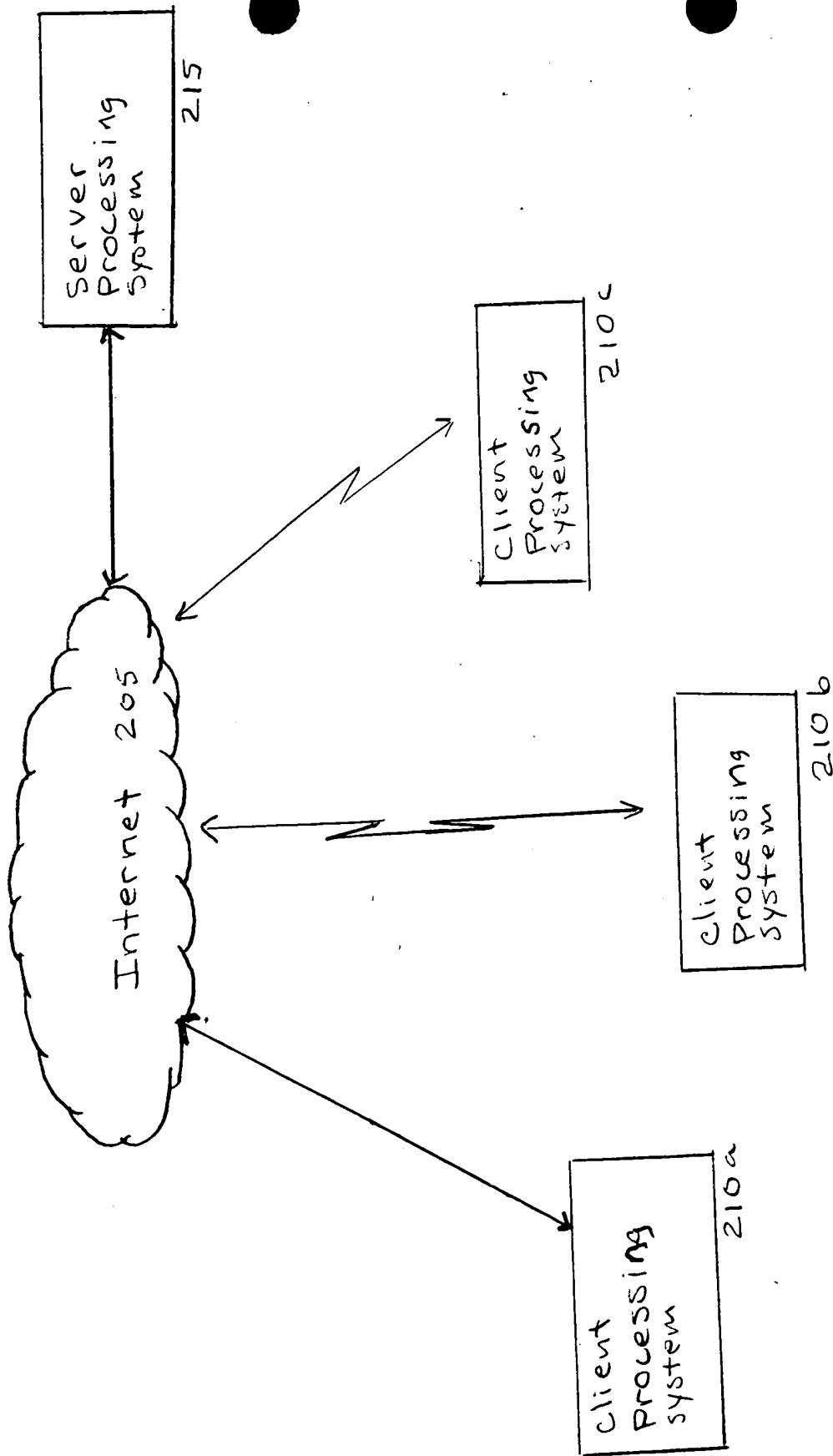
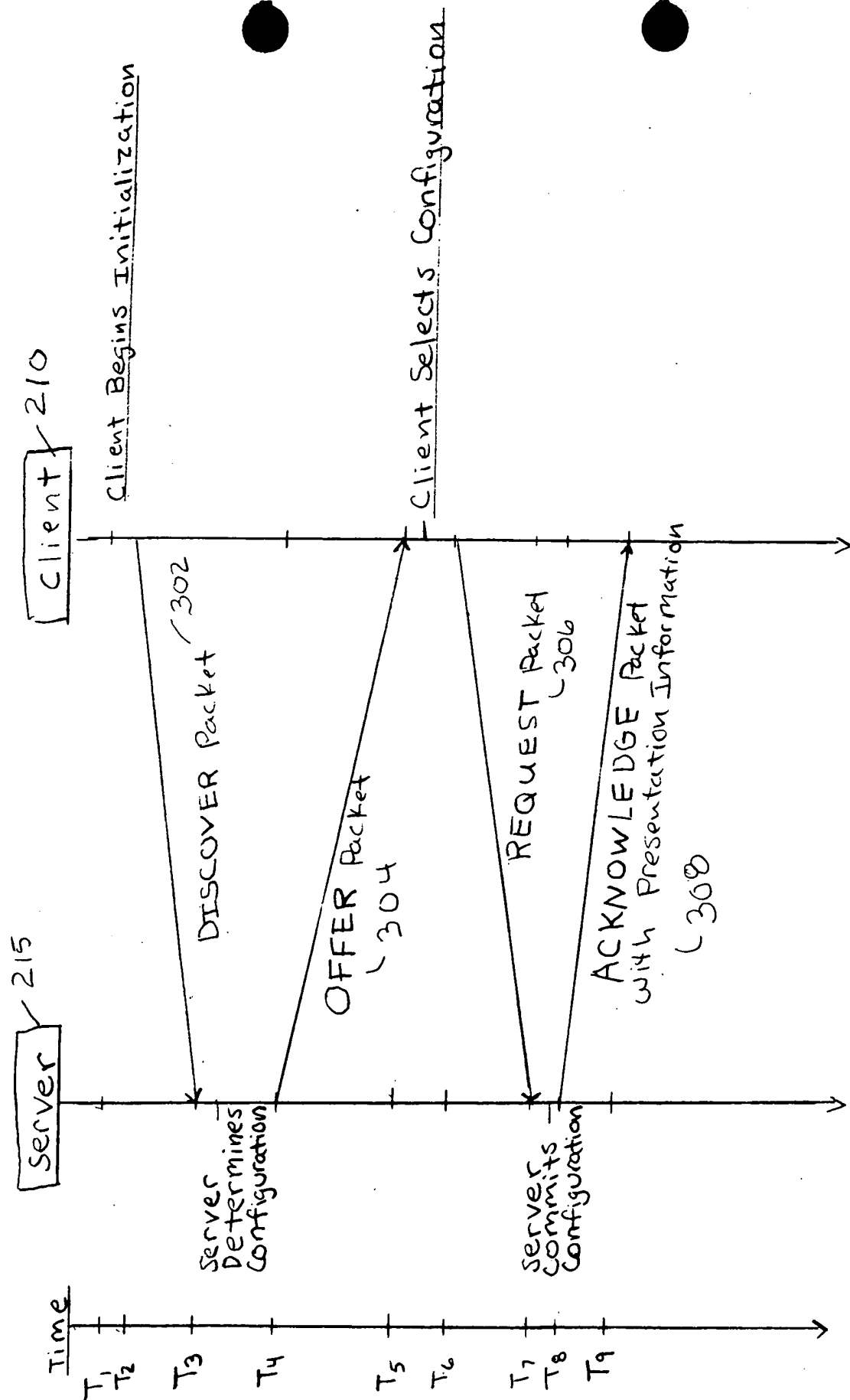


Fig. 2



Message Flow Time Line

Fig 3

TOO TO SECTION

400

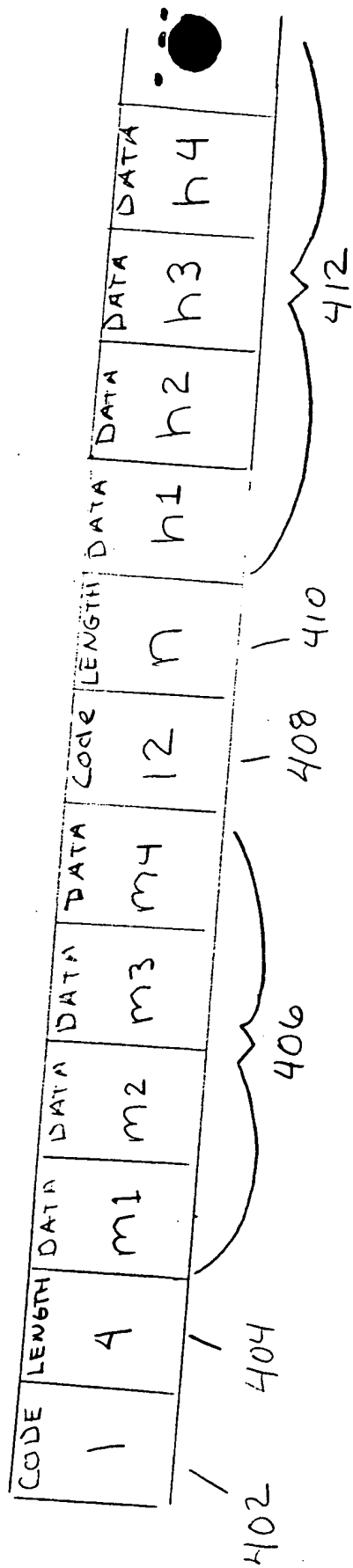


Fig. 4A

DHCP Packet Format

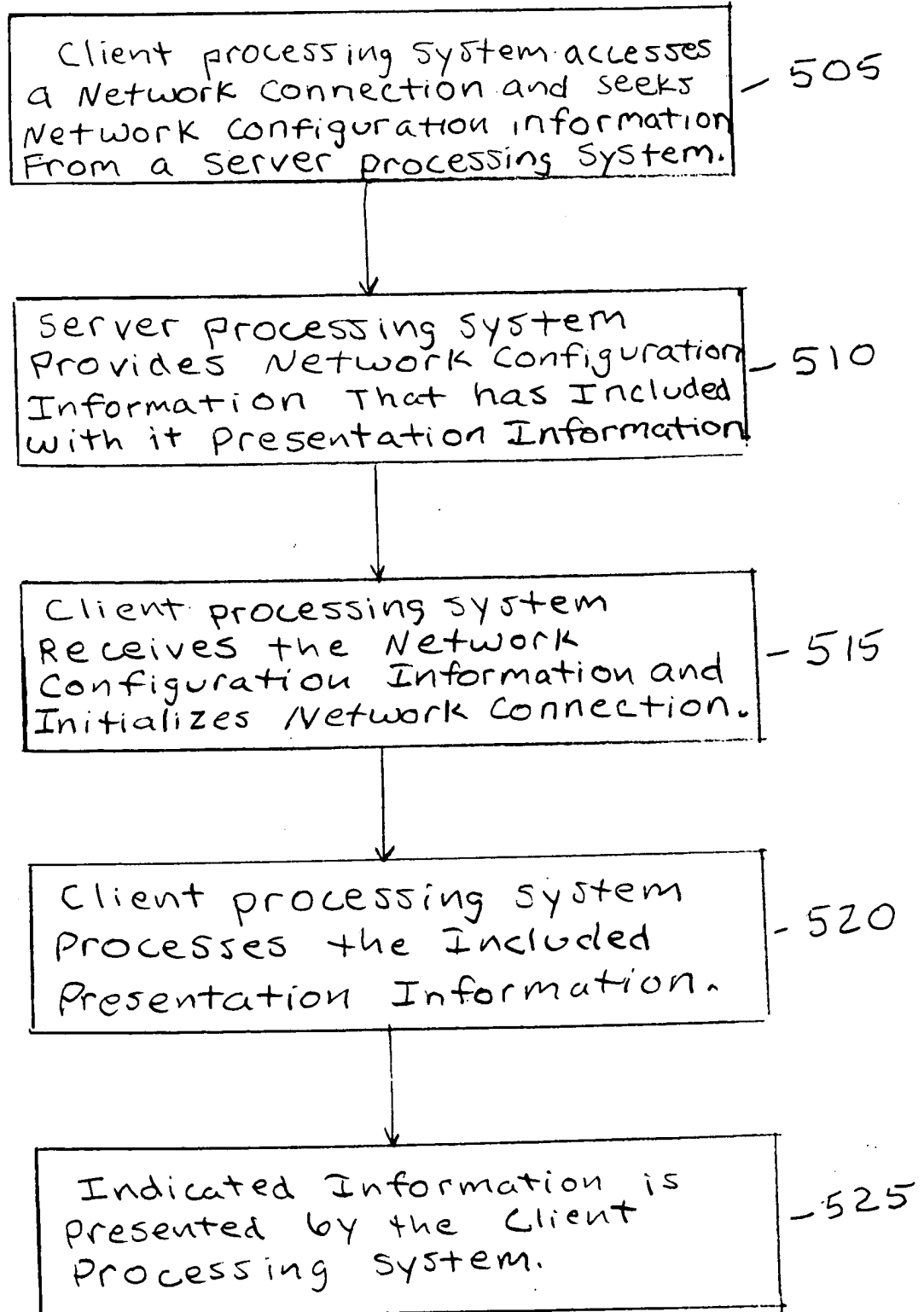


Fig. 5

Digital Processing System (DPS) Block Diagram

600

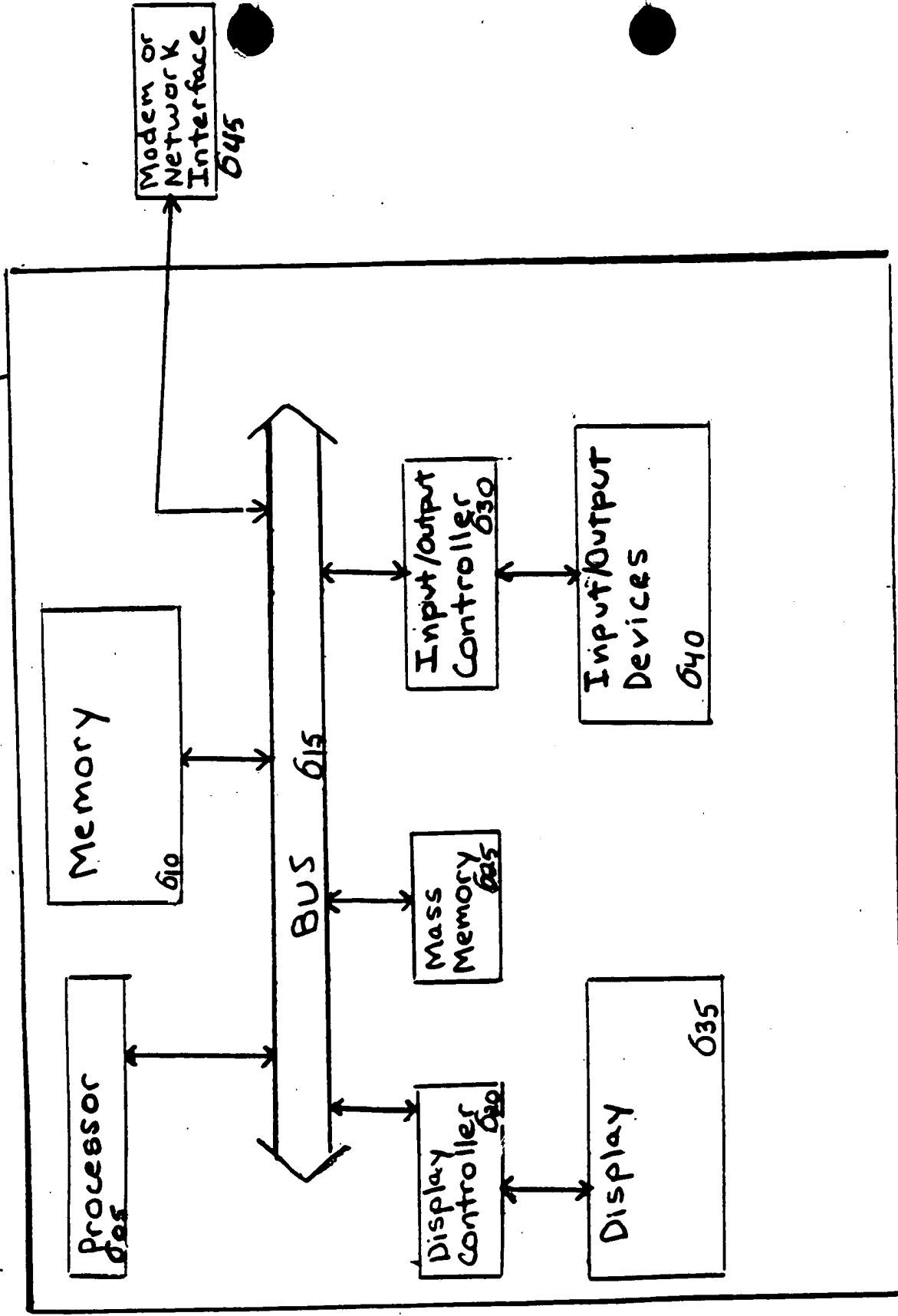


Fig. 6